



# **GCSE MARKING SCHEME**

**SUMMER 2019** 

ELECTRONICS - COMPONENT 2 C490U20-1

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## INTRODUCTION

This marking scheme was used by WJEC for the 2019 examination. It was finalised after detailed discussion at examiners' conferences by all the examiners involved in the assessment. The conference was held shortly after the paper was taken so that reference could be made to the full range of candidates' responses, with photocopied scripts forming the basis of discussion. The aim of the conference was to ensure that the marking scheme was interpreted and applied in the same way by all examiners.

It is hoped that this information will be of assistance to centres but it is recognised at the same time that, without the benefit of participation in the examiners' conference, teachers may have different views on certain matters of detail or interpretation.

WJEC regrets that it cannot enter into any discussion or correspondence about this marking scheme.

## **GCSE ELECTRONICS**

## **COMPONENT 2 – Application of Electronics – JUNE 2019**

# MARK SCHEME

## **GENERAL INSTRUCTIONS**

## Recording of marks

Examiners must mark in red ink.

One tick must equate to one mark (except for the extended response question).

Question totals should be written in the box at the end of the question.

Question totals should be entered onto the grid on the front cover and these should be added to give the script total for each candidate.

### Marking rules

All work should be seen to have been marked.

Marking schemes will indicate when explicit working is deemed to be a necessary part of a correct answer.

Crossed out responses not replaced should be marked.

Credit will be given for correct and relevant alternative responses which are not recorded in the mark scheme.

#### Extended response question

A level of response mark scheme is used. Before applying the mark scheme please read through the whole answer from start to finish. Firstly, decide which level descriptor matches best with the candidate's response: remember that you should be considering the overall quality of the response. Then decide which mark to award within the level. Award the higher mark in the level if there is a good match with both the content statements and the communication statement.

#### Marking abbreviations

The following may be used in marking schemes or in the marking of scripts to indicate reasons for the marks awarded.

cao = correct answer only ecf = error carried forward

Question	Marking dataila		Marks available						
Question	Marking details	AO1	AO2	AO3	Total	Maths			
Question          1       (a)         1       (a)	Marking details	A01		1		Maths			
	12? YES Rest of programme								
	1 mark for each correct box	3			3				

Question	Marking dataila		Marks available						
Question	Marking details	AO1	AO2	AO3	Total	Maths			
	Start Reset the counter Has a can passed? YES Add 1 to 'count' Is count equal to 12? YES Rest of programme								
	1 mark for each correc	t link 2			2				
	Question 1 Total	5	0	0	5	0			

Question	Merking dataile		М	arks availa	able	
Question	Marking details	AO1	AO2	AO3	Total	Maths
2 (a)	$V_{out} (V) $ $\int_{0}^{4} \int_{0}^{4} \int_{0}^{4}$	1	2		3	
<i>(b)</i> i	$T = 1.1 \times R \times C \qquad (1 \text{ selection})$					
	$R = \frac{T}{1.1 \times C}$ (1 re-arrangement) $R = \frac{180}{1.1 \times 2200 \times 10^{-6}}$ (1 substitution) $R = 74380.16\Omega$ (1 answer)	1	3		4	4
ii	Use / Add a variable resistor and adjust this until the exact delay is achieved.	1	0		1	
	Question 2 Total	3	5	0	8	4

	ucotia		Marking dataila		М	arks availa		
Q	uestic	on	Marking details	AO1	AO2	AO3	Total	Maths
3	(a)	i	$R_{F} \text{ from output to inverting input - (1)} R_{I} \text{ from inverting input - (1)} V_{IN} \text{ to non-inverting input - (1)}$		3		3	
		ii	$Gain = 1 + \frac{R_F}{R_1}$ $35 = 1 + \frac{R_F}{R_1} \text{ (Formula \& substitution - 1)}$ $35 - 1 = \frac{R_F}{R_1}$ $R_F = 34R_1 \text{ (Rearrange and correct ratio - 1)}$					
			$R_1 = 2k\Omega$ , $R_F = 68k\Omega$ (Correct values >=1k - 1)	1	0	2	3	3

	weatio	estion Marking details			Marks available							
	luesiic	211		AO1	AO2	AO3	Total	Maths				
3	(b)	i	Sine wave with same frequency as $V_{IN} - (1)$ Non – inverted - (1) Max amplitude at ±10.5V – (1)	2	1		3	1				
		ii	Output clipped at ±13V		1		1	1				
			Question 3 Total	3	5	2	10	5				

0	uestic	<b>o</b> n				Mork	ina data	ile				Marks available					
Q	uestic	ы				IVIA I K	ing deta	115			AO1 AO2 AO3 Total M						
4		i	Diaplay	AND Display Segments													
			Display	gate output	а	b	с	d	е	f	g						
			h	1	0	0	1	0	1	1	1						
			t	0	0	0	0	1	1	1	1						
			1 mark for	each corre	ect row in	ı table							2		2		

Question	Marking dataila		Μ	arks availa	ble	
Question	Marking details	A01	AO2	AO3	Total	Maths
ii	9V o PP1 DS1 PP1 DS					
	$_{0V}$ o	1	1	2	4	
	Question 4 Total	1	3	2	6	0

•	ussti	<b>0</b> 12				Mor	king dat	oilo					Marks	available		
Q	uesti	on				War	king det	ans				AO1	AO2	AO3	Total	Maths
5	(a)	i	Red = A +	·В									1		1	1
		ii	Yellow = (	$\bar{C} + D$									1		1	1
	(b)	i	Connectio Connectio						te - 1			2	0		2	
		ii	Pulse		Counter	outputs			Out	puts						
			number	D	С	В	Α	Red	Blue	Green	Yellow					
			0	0	0	0	0	0	1	1	1					
			1	0	0	0	1	1	1	1	1					
			2	0	0	1	0	1	0	1	1					
			3	0	0	1	1	1	0	1	1					
			4	0	1	0	0	0	1	0	0					
			5	0	1	0	1	1	1	0	0					
			6	0	1	1	0	1	0	0	0					
			7	0	1	1	1	1	0	0	0					
			8	1	0	0	0	0	1	1	1					
			9	1	0	0	1	1	1	1	1					
			10	1	0	1	0		Re	set						
			1 mark for	<sup>r</sup> each co	rrect colu	imn.						4			4	
			Question	5 Total								6	2	0	8	2

0	uestic	0.12	Marking dataila		Marks available			
Q	uestic	on	Marking details	AO1	AO2	AO3	Total	Maths
6	(a) (b)	i	The Schmitt trigger provides a fast changing output to drive the transducer driver rapidly from Off to On OR Provides a tolerance to shadows so prevents rapid switch on and off through slightly varying light levels. (1)	1	0	0	1	
			$\begin{array}{c} \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\$	2	2	0	4	2
		ii	Lights OFF – 06:00 (6:00 a.m.) – 1 Lights ON – 20:00 (8.00 p.m.) – 1		2		2	2
			Question 6 Totals	3	4	0	7	4

Question	Marking dataila		Μ	arks availa	ble	
Question	Marking details	AO1	AO2	AO3	Total	Maths
<b>7</b> (a) i	$\frac{T_{ON}}{T_{OFF}} = \frac{R_1 + R_2}{R_1} \qquad \text{(formula = 1)}$					
	$\frac{T_{ON}}{T_{OFF}} = \frac{10+47}{10}$ (equation and substitution – 1)					
	$\frac{T_{ON}}{T_{OFF}} = \frac{57}{10}$					
	$T_{ON} = 5.7T_{OFF}$ (Correct ratio – 1)	1	2		3	2
ii	$f = \frac{1.44}{(R_1 + 2R_2) \times C}$ (formula = 1)					
	$f = \frac{1.44}{(47k + 2 \times 10k) \times 220\mu}$					
	$f = \frac{1.44}{(47 \times 10^3 + 2 \times 10 \times 10^3) \times 220 \times 10^{-6}}$ (substitution and multipliers – 1)					
	$f = \frac{1.44}{(67 \times 10^3) \times 220 \times 10^{-6}} = 0.0976 \cong 0.1 Hz$ (answer – 1)					2
		3			3	3

Question			Marking dataila				М	arks availa	ble	
Question			Marking details			AO1	AO2	AO3	Total	Maths
<i>(b)</i> i	Decade Counter Outputs	Red	Yellow	Green	Blue					
	0	1	0	0	0					
	1	0	1	0	0					
	2	0	0	0	1					
	3	1	0	0	0					
	4	0	0	0	1					
	5	0	1	0	0					
	6	0	0	1	0					
	7		RES	SET						
	8									
	9									
	1 mark for each c	orrect column				4			4	

Question	Marking dataila		М	arks availa	ble	
Question	Marking details	AO1	AO2	AO3	Total	Maths
<i>(b)</i> ii	Pulse Generator					
iii	1 mark for each correctly configured output x 4 $f = \frac{1}{T}$ (Formula - 1)		4		4	
	$T = \frac{1}{f}$ (Rearrangement - 1) $T = \frac{1}{0.1} = 10$ (Substitution & Answer - 1) Blue on for 2 clock pulses = 2 x 10 = 20s (Final answer - 1)					
	Question 7 Total	1 9	3 9	0	4 18	4 9

Question		Mauking datalla					
Q	uestion	Marking details         A – Preamplifier – 1         B – Mixer - 1         C – Power Amplifier – 1	AO1	AO2	AO3	Total	Maths
8	(a)		3				
	(b)	R3 1k 1k Input 1 •R2 R2 1k Input 2 •		2	2		
	(C)	Block C takes the mixed output signal and either increases the <b>current</b> or <b>power</b> needed to drive the loudspeaker.		1			
		Question 8 total	3	3	2	8	

Question							
G	luestio	Marking details	AO1	AO2	AO3	Total	Maths
9	(a)	Indicative Content:The design as presented has a couple of issues. Firstly, the heater should switch on when either the temperature falls below 25°C OR a switch is pressed. The design incorporates an AND gate which will not meet the requirement of the specification and needs to be changed for an OR gate. The switch SW1 is correctly orientated to provide a logic 1 input at A when pressed as the resistor acts as a pull-down resistor holding input A at logic 0 until the switch is pressed as per the specification. The thermistor however is incorrectly placed as it should be connected between B and the 0V line to provide the logic 1 signal required when the temperature is below 25°C as the resistance of the thermistor increases when the temperature falls. The MOSFET and heater are correctly positioned.					
		<ul> <li>5-6 marks</li> <li>Candidates have evaluated the specification fully e.g. all three statements considered against the given circuit, justifying placement and selection of components in the design and given a comprehensive description of changes that should be made. Description has a logical thought process and presentation.</li> <li>There is a sustained line of reasoning which is coherent, substantiated and logically structured. The information included in the response is relevant to the argument.</li> </ul>					
		<b>3-4 marks</b> Candidates have evaluated most of the specification e.g. two of the three statements with the given circuit, justification of placement and selection of components in the design may be less clear and given a brief description of changes that should be made. Description has a reasonable thought process and presentation.					
		There is a line of reasoning which is partially coherent, supported by some evidence and with some structure. Mainly relevant information is included in the response but there may be some minor errors or the inclusion of some information not relevant to the argument.	0	2	4	6	

Question	Marking details1-2 marks Candidates have evaluated little of the specification e.g. one of the three statements with the given circuit, justification of placement and selection of components in the design may be weak and limited description of changes that should be made. Description has a random presentation.There is a basic line of reasoning which is not coherent, supported by limited evidence and with very little structure. There may be significant errors or the inclusion of information not relevant to the argument.0 marks No evaluation of circuit against specification or any statement of value.					
Question		AO1	AO2	AO3	Total	Maths
	Response not creditworthy or not attempted.					
(b)	$I_D = g_M (V_{GS} - 3) (1 - \text{equation})$ $(V_{GS} - 3) = \frac{I_D}{g_M}$ $V_{GS} = \frac{I_D}{g_M} + 3  (1 - \text{rearrangement})$ $V_{GS} = \frac{4}{0.8} + 3  (1 - \text{substitution})$ $V_{GS} = 8V  (1 - \text{answer})$	2	2		4	3
	Question 9 total	2	4	4	10	3
	TOTAL	35	35	10	80	24

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